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PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10047017	FILING DATE 01/15/2002	CLASS 333	SUBCLASS	GAU 2817	EXAMINER <i>D. TAKAOKA</i>
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**CONTINUING DATA VERIFIED:

** FOREIGN APPLICATIONS VERIFIED:

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input type="checkbox"/> yes <input type="checkbox"/> no	ATTORNEY DOCKET NO
35 USC 119 conditions met		<input type="checkbox"/> yes <input type="checkbox"/> no	4925-149
Verified and Acknowledged Examiner's initials			
TITLE : Circuit topology for attenuator and switch circuits			
U.S. DEPT. OF COMM./PAT. & TM PTO-456L (Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING		
Amount Due	Date Paid	Sheets Drwg.		Figs. Drwg.
TERMINAL DISCLAIMER		Print Fig.		
		Application Examiner		
		PREPARED FOR ISSUE		
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